

(19)



JAPANESE PATENT OFFICE

PATENT ABSTRACTS OF JAPAN

(11) Publication number: 09162553 A

(43) Date of publication of application: 20.06.97

(51) Int. Cl.

H05K 3/46

H05K 3/40

(21) Application number: 07324175

(71) Applicant: TOSHIBA CORP

(22) Date of filing: 13.12.95

(72) Inventor: TANAKA KAZUYASU
SASAKI KENJI

(54) MANUFACTURE OF MULTILAYER PRINTED WIRING BOARD

conductive metallic foils 1a, 1b being positioned as outer layers.

(57) Abstract:

COPYRIGHT: (C)1997,JPO

PROBLEM TO BE SOLVED: To provide a method for manufacturing high-reliability printed wiring boards with a high yield rate capable of performing high-density wiring and packaging, simplifying the manufacturing process.

SOLUTION: First of all, the main surface of a synthetic resin sheet 4a is opposed to the wiring pattern 2a forming surface of a wiring raw board 3 having a conductive metal foil 1a on one main surface and having a wiring pattern 2a on the other main surface, and arranged like a laminate. Next a conductive metal foil 1b having conductive bumps 5a for forming electric connections with the wiring pattern 2a on one main surface is positioned, arranged, and laminated on the main surface of the synthetic resin sheet 4a. And pressure is applied to the laminated substance, and the tips of the conductive bumps 5a are inserted into the synthetic resin sheet in the direction of its thickness, and a through type conductor wiring part 5 for the wiring pattern 2a opposed is formed. And wiring patterning 2b is performed by selectively etching both

